Odin TeleSystems Inc.

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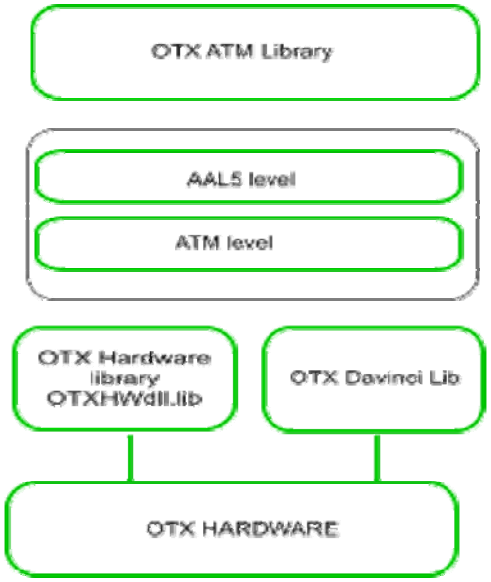
OtxATM Library

The OtxATM library is a software library that provides decoding and encoding functionality of the Asynchronous Transfer Mode (ATM) protocol for products in the industry award-winning Odin Telecom frameworX (OTX) family. This library is supported by multiple variants of Odin's OTX PCI, PCIe, and ExpressCard/54 T1/E1 adapter boards as well as the stand-alone Alvis-CSI product.

The OtxATM library can be run simultaneously on multiple OTX boards and multiple channels. Further, it can operate with any combinations of timeslots over multiple T1/E1 spans.

Both permanent and dynamic connections can be created. The library also supports a large number of virtual AAL5 channels with specific VPI/VCI identifier.

So if you are looking for a powerful ATM decoding or encoding library, the OtxATM library delivers results in a complete and value-based package.



Feature Highlights

- Interaction between low-level and high-level protocols (e.g. IP, ICMP, SS7), using burst functionality of Odin's OTX cards.
- OTX ATM and AAL5 logical devices.
- Uses Burst DMA to access the T1/E1 data.
- ATM descrambler in E1 mode is supported (G.804).
- T1/E1 unframed, framed and fractional modes are supported.
- Uses ATM and AAL 5 layers.

OtxATM Library Product Brief

Hardware and Software Specifications	
The library is supported by the following operating systems:	• Win32, Win64 (Windows 2000/ XP/ 2003 Server/Vista) • Linux (x86, ARM Davinci MontaVista)
The library can be configured to run with the following Odin TeleSystems' board combinations:	• Thor-2-ExpressCard for 2 full T1 or E1 spans. • Thor-4-ExpressCard for 4 unidirectional T1 or E1 spans. • Thor-2-PCI-Plus for 2 full T1 or E1 spans. • Thor-2-PCI-Express for 2 full T1 or E1 spans. • Thor-8-PCI-Plus for 8 full T1 or E1 spans. • Sleipnir-1-PCI-Plus for 1 full T1 or E1 spans. • Gimle-16-PCI-Plus for 16 unidirectional E1 spans. • Sleipnir-1-PC104-Plus for 1 full T1 or E1 spans. • Alvis-ASM. • Alvis-PCIe • Alvis-CSI for 4 or 8 full T1 or E1 spans.
Features	
Decoding:	• Decoded ATM cells are delivered via standard OTX API functions (Events etc). • Multiple configurable Virtual Paths (VP) and Virtual Channels (VC). • Supporting the ATM HSL descrambler (G.804). • Optimized ATM/AAL5 CRC/HEC check. • Detection of Unhandled VP/CV cells
Encoding:	• Multiple ATM cells queued for transmission. • Multiple configurable Virtual Paths (VP) and Virtual Channels (VC). • Supporting the ATM HSL scrambler. • Automatic CRC generation.
Other features	
	• Standard OTX API on top of the OTX HW SDK. • Supports Network-to-Network Interface (NNI) and Permanent Virtual Channels (PVC) endpoints. • Supports ATM User-to-User byte (AUU). • Supports multiple ATM channels (selectable timeslots of the T1 or E1 span). • Low CPU load consumption per channel. • Multi-board/multi-span/multi-ATM/multi-AAL5 configurations are possible . • Implements ATM layer (I.361, I.432.1), AAL5 (I.363.5). • OTX Burst DMA compatible API with a flexible timeslots mapping
Ordering Information	
Product Name / Product Category	• OtxATM library / SMA-1030-1
Contact Information	
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